

METHOD AND SYSTEM FOR VERIFYING THE HARDWARE IMPLEMENTATION OF TCP/IP

ABSTRACT

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The present invention provides for a method and system for verifying hardware operation of an Application Specific Integrated Circuit ("ASIC") chip. The ASIC includes microcode logic for enabling Transmission Control Protocol/Internet Protocol ("TCP/IP") processing. The method is performed in a system that includes a first computing device having a processor and computer code for simulating a computing device that includes the ASIC. Wherein the ASIC is tested against a conventional TCP/IP stack included in a second computing device coupled to the first computing device.

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